

SARDAR VALLABHBHAI NATIONAL INSTITUTE OF TECHNOLOGY
Ichchhanath, Surat-395007 (Gujarat), India

REPORT SHEET SHOWING RESULT OF CANDIDATES APPEARED AT M.TECH. I FIRST SEMESTER COMMUNICATION SYSTEM AT REGULAR EXAM HELD DURING NOV/DEC 2025

					1	2	3	4	5	6	7	8	9	10	11	12	13	14	Remark
Semester	Student Name				Course Code	ECCS101 T	ECCS103 T	ECCS105 T	ECCS107 P	ECCS109 P	ECCS111 T	ECVL101 T							
					Credit	3.00	3.00	3.00	3.00	2.00	3.00	3.00							
	Admission Number	Reg-Credit	Earn-Credit	SGPA	CGPA	RF CIRCUITS & SYSTEMS	ADVANCE OPTICAL COMMUNICATION SYSTEMS	ADVANCE DSP	COMMUNICATION LAB-I	SEMINAR	INFORMATION THEORY & CODING	DIGITAL VLSI DESIGN							
Semester : 1 ANAMIKA KUMARI						BC	BC	BB	AB	BB	BB	CC							
P25EC001						20.00	20.00	7.55	7.55										
Semester : 1 PRERNA LAXMINARYAN SHARMA						BB	BB	BB	AB	AB	AB	CD							
P25EC002						20.00	20.00	7.95	7.95										
Semester	Student Name				Course Code	ECCS101 T	ECCS103 T	ECCS105 T	ECCS107 P	ECCS109 P	ECVL101 T	ECVL105 T							
					Credit	3.00	3.00	3.00	3.00	2.00	3.00	3.00							
	Admission Number	Reg-Credit	Earn-Credit	SGPA	CGPA	RF CIRCUITS & SYSTEMS	ADVANCE OPTICAL COMMUNICATION SYSTEMS	ADVANCE DSP	COMMUNICATION LAB-I	SEMINAR	DIGITAL VLSI DESIGN	EMBEDDED SYSTEMS							
Semester : 1 VISHAL LAMANI						BC	BB	BB	AB	BB	CC	CC							
P25EC003						20.00	20.00	7.40	7.40										
Semester : 1 BHAVYA PRASANT DESAI						AB	AA	AA	AB	AB	BC	AA							
P25EC005						20.00	20.00	9.15	9.15										
Semester	Student Name				Course Code	ECCS101 T	ECCS103 T	ECCS105 T	ECCS107 P	ECCS109 P	ECCS111 T	ECVL101 T							
					Credit	3.00	3.00	3.00	3.00	2.00	3.00	3.00							
	Admission Number	Reg-Credit	Earn-Credit	SGPA	CGPA	RF CIRCUITS & SYSTEMS	ADVANCE OPTICAL COMMUNICATION SYSTEMS	ADVANCE DSP	COMMUNICATION LAB-I	SEMINAR	INFORMATION THEORY & CODING	DIGITAL VLSI DESIGN							
Semester : 1 HARSHA VARDHAN RAVI						BC	CD	BC	BB	AB	BC	FF							BACKLOG : 3 CREDIT(S)
P25EC006						20.00	17.00	6.00	7.06										
Semester	Student Name				Course Code	ECCS101 T	ECCS103 T	ECCS105 T	ECCS107 P	ECCS109 P	ECVL101 T	ECVL105 T							
					Credit	3.00	3.00	3.00	3.00	2.00	3.00	3.00							
	Admission Number	Reg-Credit	Earn-Credit	SGPA	CGPA	RF CIRCUITS & SYSTEMS	ADVANCE OPTICAL COMMUNICATION SYSTEMS	ADVANCE DSP	COMMUNICATION LAB-I	SEMINAR	DIGITAL VLSI DESIGN	EMBEDDED SYSTEMS							
Semester : 1 KARTIK SWAMY						AB	AB	AB	AA	AA	BC	AA							
P25EC007						20.00	20.00	9.10	9.10										

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					1	2	3	4	5	6	7	8	9	10	11	12	13	14	Remark
Semester	Student Name				Course Code	ECCS101 T	ECCS103 T	ECCS105 T	ECCS107 P	ECCS109 P	ECCS111 T	ECVL101 T							
					Credit	3.00	3.00	3.00	3.00	2.00	3.00	3.00							
Admission Number	Reg-Credit	Earn-Credit	SGPA	CGPA	RF CIRCUITS & SYSTEMS	ADVANCE OPTICAL COMMUNICATION SYSTEMS	ADVANCE DSP	COMMUNICATION LAB-I	SEMINAR	INFORMATION THEORY & CODING	DIGITAL VLSI DESIGN								
Semester : 1 GANDURI SHASHANK						BC	BB	BB	AB	BB	BB	CD							
P25EC008 20.00 20.00 7.55 7.55																			
Semester : 1 MOHIT PATLE						BB	BB	AB	AB	AB	AB	CC							
P25EC009 20.00 20.00 8.25 8.25																			
Semester : 1 SHIVANGI VYAS						BB	BB	BB	AB	AA	AB	DD							
P25EC010 20.00 20.00 7.90 7.90																			
Semester	Student Name				Course Code	ECCS101 T	ECCS103 T	ECCS105 T	ECCS107 P	ECCS109 P	ECVL101 T	ECVL105 T							
					Credit	3.00	3.00	3.00	3.00	2.00	3.00	3.00							
Admission Number	Reg-Credit	Earn-Credit	SGPA	CGPA	RF CIRCUITS & SYSTEMS	ADVANCE OPTICAL COMMUNICATION SYSTEMS	ADVANCE DSP	COMMUNICATION LAB-I	SEMINAR	DIGITAL VLSI DESIGN	EMBEDDED SYSTEMS								
Semester : 1 ARCHIE RAI						BC	BC	BC	AB	AB	FF	DD							BACKLOG : 3 CREDIT(S)
P25EC011 20.00 17.00 6.00 7.06																			
Semester : 1 KOMAL PADIA						CC	DD	CC	BB	BC	FF	FF							BACKLOG : 6 CREDIT(S)
P25EC012 20.00 14.00 4.30 6.14																			

Total Student Count : 11